ENGR 430 – Homework #4

- Ex. 2.22 p. 111

The following assignment addresses Course Learning Objective #7.

*Explain the role of design margin and reliability (ABET criteria 3i, 3j)*

- Ex. 2.15 p. 110

The following assignments address Course Learning Objective #6.

*Model and estimate the propagation delay of CMOS gates (ABET criteria 3a, 3c)*

- Ex. 4.27 p. 269
- Ex. 4.28 p. 269