Eastern Washington University  
Engineering and Design Department  
Course Description

Number: ENGR 331 (Section 01)  
Title: Microelectronics II  
Credits: 5 credits  
Course Format: Four one-hour lectures per week. One two-hour lab. per week.  
Coordinator: Claudio Talarico, Ph.D., Assistant Professor  
Computer Engineering Building, Room 336  
e-mail: ctalarico@ewu.edu  
Tel.: 509-359-4780  

Catalog Description:  
Microelectronics II is the second course in modeling and designing circuits and systems based on semiconductor devices. The emphasis is on current mirrors, differential stages, frequency response, and feedback effects.

Learning Objectives (and corresponding mapping to ABET Criteria 3): Upon completion of this course, students will be able to:  
1. Design current source circuits to provide a specified current and output resistance (ABET 3c).  
2. Derive the system transfer function of circuits. Develop the Bode Diagrams (ABET 3a).  
3. Analyze the frequency response of amplifying circuits (ABET 3b).  
4. Describe and analyze the characteristics of the basic differential amplifier (ABET 3j).  
5. Describe feedback concepts, in general terms: advantages and disadvantages of using feedback (ABET 3j).  
6. Analyze and Design Basic Oscillator circuits (ABET 3b, 3c, 3e)  
7. Use CAD tools such as SPICE to model, analyze, simulate, design and improve the functionality of semiconductor devices, circuits, and systems (ABET 3b, 3e, 3g, 3i, 3j, 3k).

Prerequisites: ENGR 330 (Microelectronics II) or consent of the instructor.

Topics:  
- Current Mirrors  
- Differential and Multistage Amplifiers  
- Frequency Response  
- Feedback  
- Oscillators  

Computer Usage:  
1. Extensive CAD tool use for Design Entry and Simulation.  
2. Design projects and laboratory assignments involving formal technical reports requiring the use of word processing and graphics software for their presentation.

Laboratory:  
Laboratory experiments and design assignments require an understanding of basic semiconductor devices and circuit analysis and design and involve the use of CAD tools, personal computers, writing skills and teamwork.
Grading:  
A = 3.5−4.0 (90−100%), B = 3.0−3.4 (80−89%), C = 2.0−2.9 (70−79%),
D = 1.0−1.9 (60−69%), F= 0.0 (0−59%)

Homework: 10%
Labs 20%
Midterm exam: 40%
Final exam/project: 30%

Course Outcomes and mapping to ABET Criteria 3:

a. An ability to apply knowledge of mathematics, science, and engineering.
   Students are required to use their background in mathematics, physics and engineering to successfully finish homework, labs, and exams.

b. An ability to design and conduct experiments, as well as to analyze and interpret data.
   Students are required to design and implement lab experiments for analyzing, designing, and improving circuits and systems.

c. An ability to design a system component, or process to meet desired needs within realistic constraints.
   Homework, laboratory experiments, and exams require students to analyze, design, evaluate, and improve circuits and systems that must meet specified constraints.

d. An ability to function effectively on multi-disciplinary teams.
   n/a

e. An ability to identify, formulate, and solve engineering problems
   Homework and labs require students to identity, formulate, model and solve several engineering challenges.

f. An understanding of professional and ethical responsibility.
   n/a

g. An ability to communicate effectively.
   Students need to write several lab. reports.

h. Understand impact of engineering solutions in a global, economic, environmental and societal context
   n/a

i. A Recognition of the need for, and an ability to engage in life-long learning
   Students must self-learn the use of CAD tools.

j. A Knowledge of contemporary issues
   Homework, and lab. experiments are based on state of the art technologies and methodologies

k. An ability to use the techniques, skills and modern engineering tools necessary for engineering practice.
   Lab. experiments require the use of current methodologies, and CAD tools.

Prepared by: Claudio Talarico
Last Revised: March, 16, 2008