Homework #3

The next problem(s) addresses the following course learning objective(s):
• Evaluate circuits using KCL and KVL laws

1. Problem 2.22 at page 52
2. Problem 2.23 at page 52
3. Problem 2.26 at page 53
4. Problem 2.29 at page 53

The next problem(s) addresses the following course learning objective(s):
• Evaluate circuits using an equivalent resistance in series and parallel connections.
• Evaluate circuits using the voltage divider circuit and current divider circuit.

5. Problem 3.1 in Assessing Objective 1 at page 62
6. Problem 3.2 in Assessing Objective 1 at page 65
7. Problem 3.3 in Assessing Objective 1 at page 65
8. Problem 3.4 in Assessing Objective 1 at page 67
9. Problem 3.8 at page 81
10. Problem 3.9 at page 82