Homework #1

Problem 1 (10 pts)

1a. Have you read the EWU Academic Integrity Policy?
   Yes ☐ No ☐

1b. Is there any issue/question that has not been addressed in a satisfactory manner by the EWU Academic Integrity Policy and for which you would like further clarifications?
   Yes ☐ No ☐

If you answered yes to question 1b please list all your unresolved questions/issues concerning Academic Integrity.

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Problem 2 (10 pts)
Define the following acronyms:

ASIC:

CAD:

EDA:

DIP:

FPGA:

IC:

HDL:

IP:

VLSI:

VHDL:

CPLD:

PCB: